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Bhavnagarwala et al.
YOR920030290451 (TAD) (8728-636)

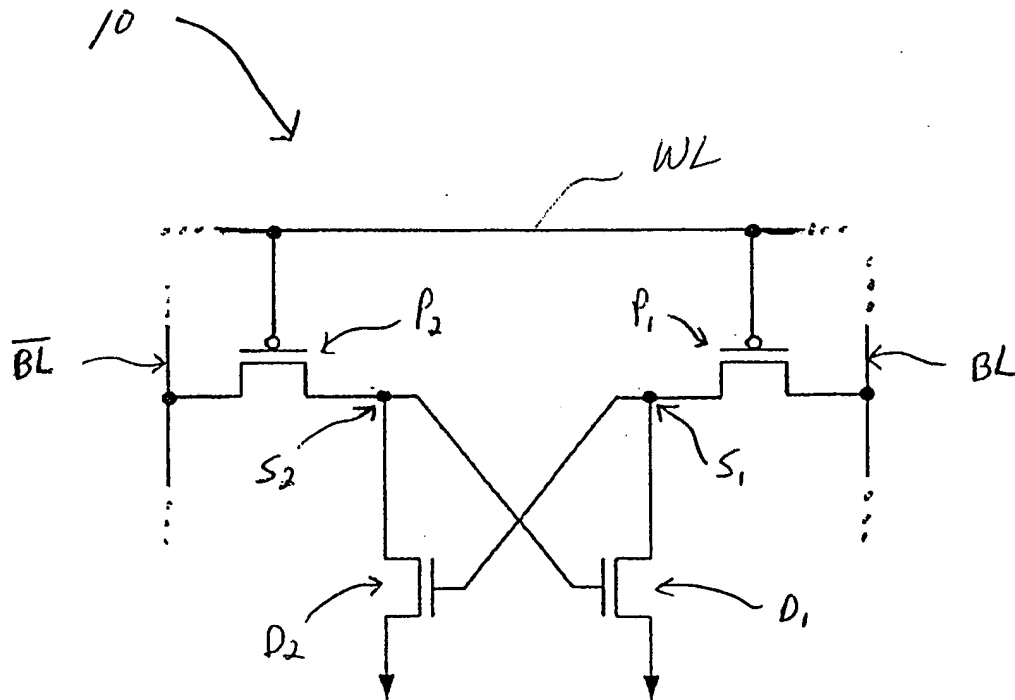
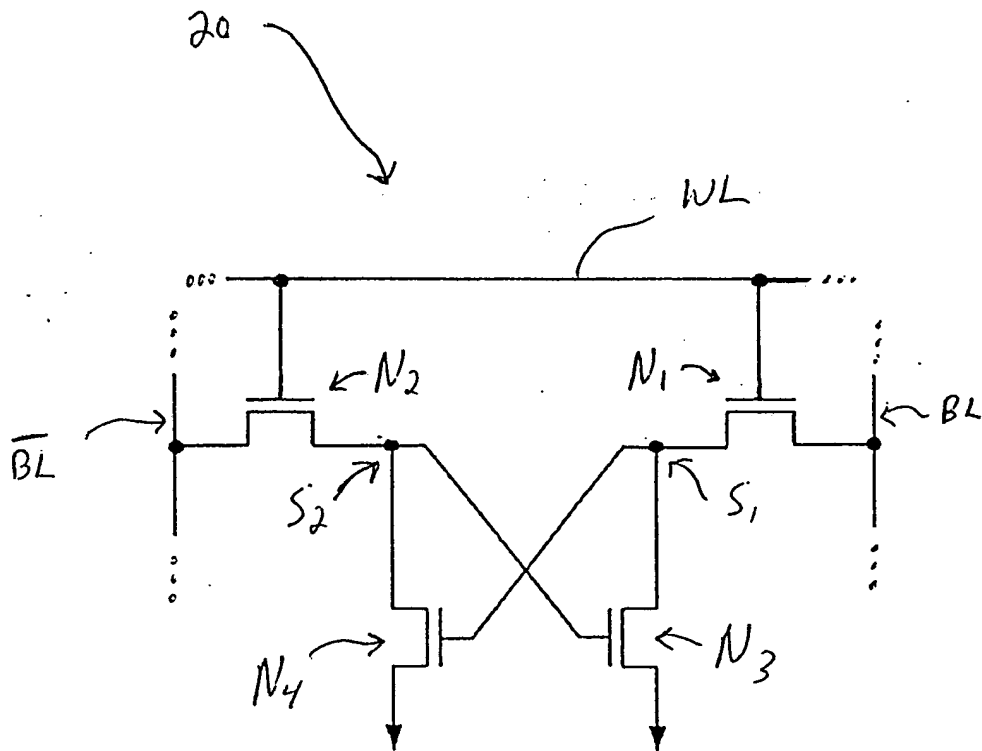


Fig. 1
Prior Art

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Y08920030290US1 (8728-636)



N_1 and N_2 - LVT NFETs

N_3 and N_4 - HVT NFETs

Fig. 2

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YDR920030290US1 (8728-636)

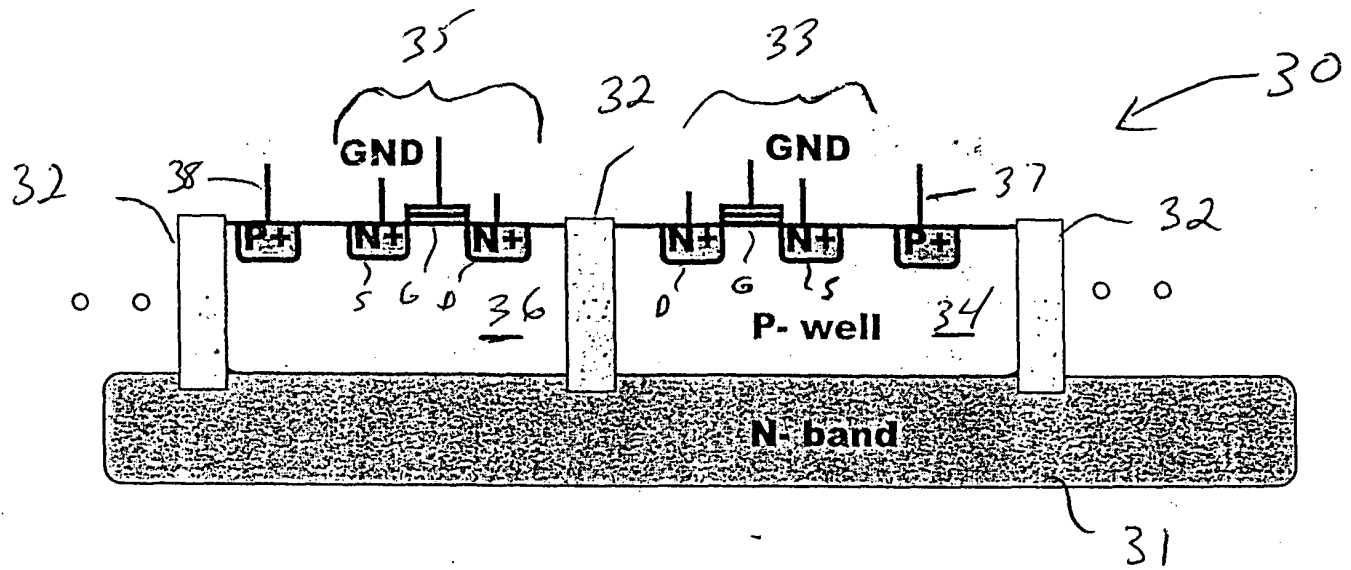


Fig. 4

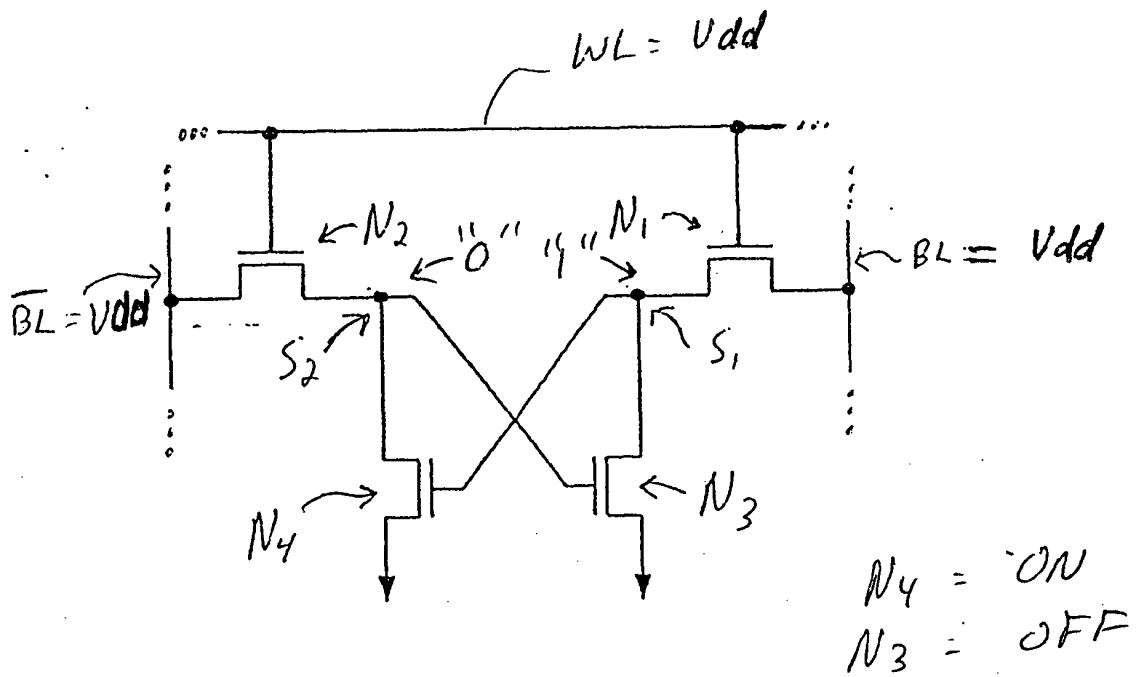


Fig. 3

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YDR920030290451 (8728-636)

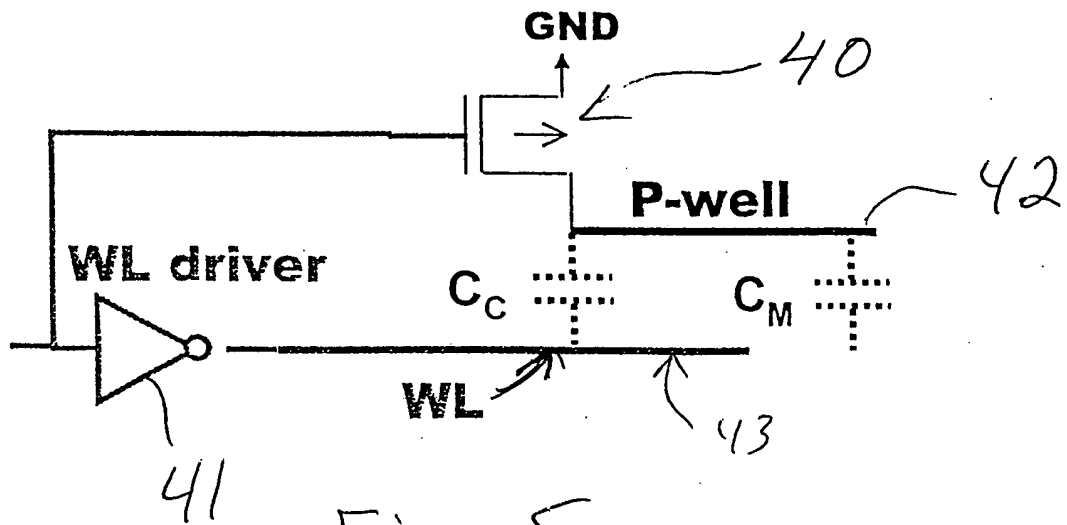
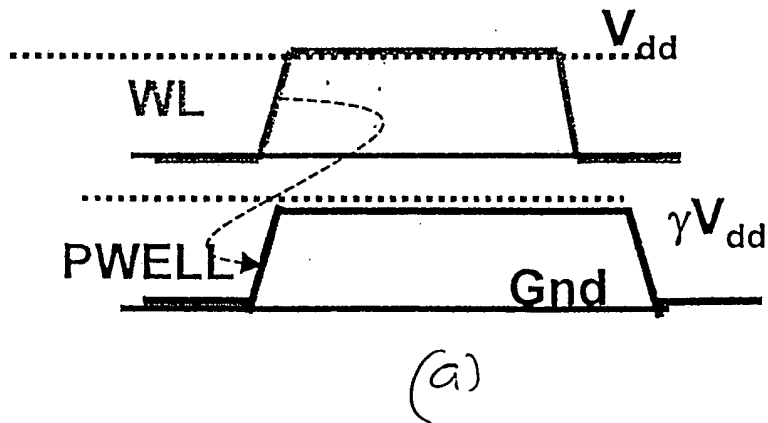


Fig. 5



(b)

$$V_{dd}\gamma = V_{dd} C_C / (C_C + C_M)$$

Fig. 6